DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below adjacent to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter that is claimed and for which a patent is sought by way of the application entitled

VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA EFFECT OF A CMOS BIPOLAR TRANSISTOR

which (check)	is attached hereto. and is amended by the Preliminary Amendment attached hereto. was filed on as Application Serial No. and was amended on (if applicable).
hereby state including the cl	that I have reviewed and understand the contents of the above identified application aims, as amended by any amendment referred to above.
acknowledge Code of Federa	the duty to disclose information, which is material to patentability as defined in Title 37 I Regulations, § 1.56.
my foreign app	foreign priority benefits under Title 35, United States Code, § 119(a)-(d) or § 365(b) o plication(s) for patent or inventor's certificate or under § 365(a) of any PCT international designating at least one country other than the United States of America, listed below and

any foreign application(s) for patent or inventor's certificate or under § 365(a) of any PCT international application(s) designating at least one country other than the United States of America, listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed:

Prior Foreign Application(s)			Priority, Claimed	
Number	Country	Day/Month/Year Filed	Yes	No
None				

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith:

Mark Zagorin (36,067); Andrew C. Graham (36,531); David W. O'Brien (40,107); Nicole Teitler Cave (54,021); Steven R. Gilliam (51,734); Edward J. Marshall (45,395), and Nestor Ho (39,460).

I declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under Title 18, United States Code, § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Please direct all correspondence concerning this application to:

USPTO Customer Number

22120

Zagorin, O'Brien & Graham, LLP

Full name of sole (or first join	int) inventor: Akhil K. Garlapati		
Inventor's Signature: Residence: Post Office Address:	Boston, MA 50 Jamaicaway, #3 Boston, MA 02130	Date: Citizenship:	3/31/04 India
Full name of second joint inv	ventor: Bruce P. Del Signore		
Inventor's Signature: Residence: Post Office Address:	Bylu Pll Jeyr Hollis, NH 31 Baldwin Lane Hollis, NH 03049	Date: Citizenship:	3/31/04 USA
Full name of third joint inver	ntor: David Pietruszynski		
Inventor's Signature: Residence: Post Office Address:	Austin, TX 6211 John Chisum Lane Austin, Texas 78749	Date: Citizenship:	USA

Aπomey Docket No.: 026-0044

DECLARATION AND POWER OF ATTORNEY

As a below ramed inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below adjacent to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter that is claimed and for which a patent is sought by way of the application entitled

VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA EFFECT OF A CMOS BIPOLAR TRANSISTOR

Prior Foreign Application(s)	Priority Claimed
I hereby claim foreign priority benefits under Title 35, United States Code, § I any foreign application(s) for patent or inventor's certificate or under § 365(a) of application(s) designating at least one country other than the United States of A have also identified below any foreign application(s) for patent or inventor's international application(s) designating at least one country other than the United by me on the same subject matter having a filing date before that of the application is claimed:	of any PCT international merica, listed below and certificate or any PCT I States of America filed
I acknowledge the duty to disclose information, which is material to patentabilit Code of Federal Regulations, § 1.56.	y as defined in Title 37,
I hereby state that I have reviewed and understand the contents of the above including the claims, as amended by any amendment referred to above.	e identified application,
which (check) is attached hereto. and is amended by the Preliminary Amendment attached hereto are filled on as Application Serial in and was amended on (if application).	٧o.

Prior Foreign Application(s)			Priority Claimed	
Number	Country	Day/Month/Year Filed	Yes	No
None				

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith:

Mark Zagorin (36,067); Andrew C. Graham (36,531); David W. O'Brien (40,107); Nicole Teitler Cave (54,021); Steven R. Gilliam (51,734); Edward J. Marshall (45,395), and Nestor Ho (39,460).

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Attorney Docket No.: 026-0044

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22120

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Full name of third joint inventor: David Pietruszynsk	g ,	~ <i>,</i>
Inventor's Signature:	Date:	3/31/04
Residence: Austin, TX	Citizenship:	USA